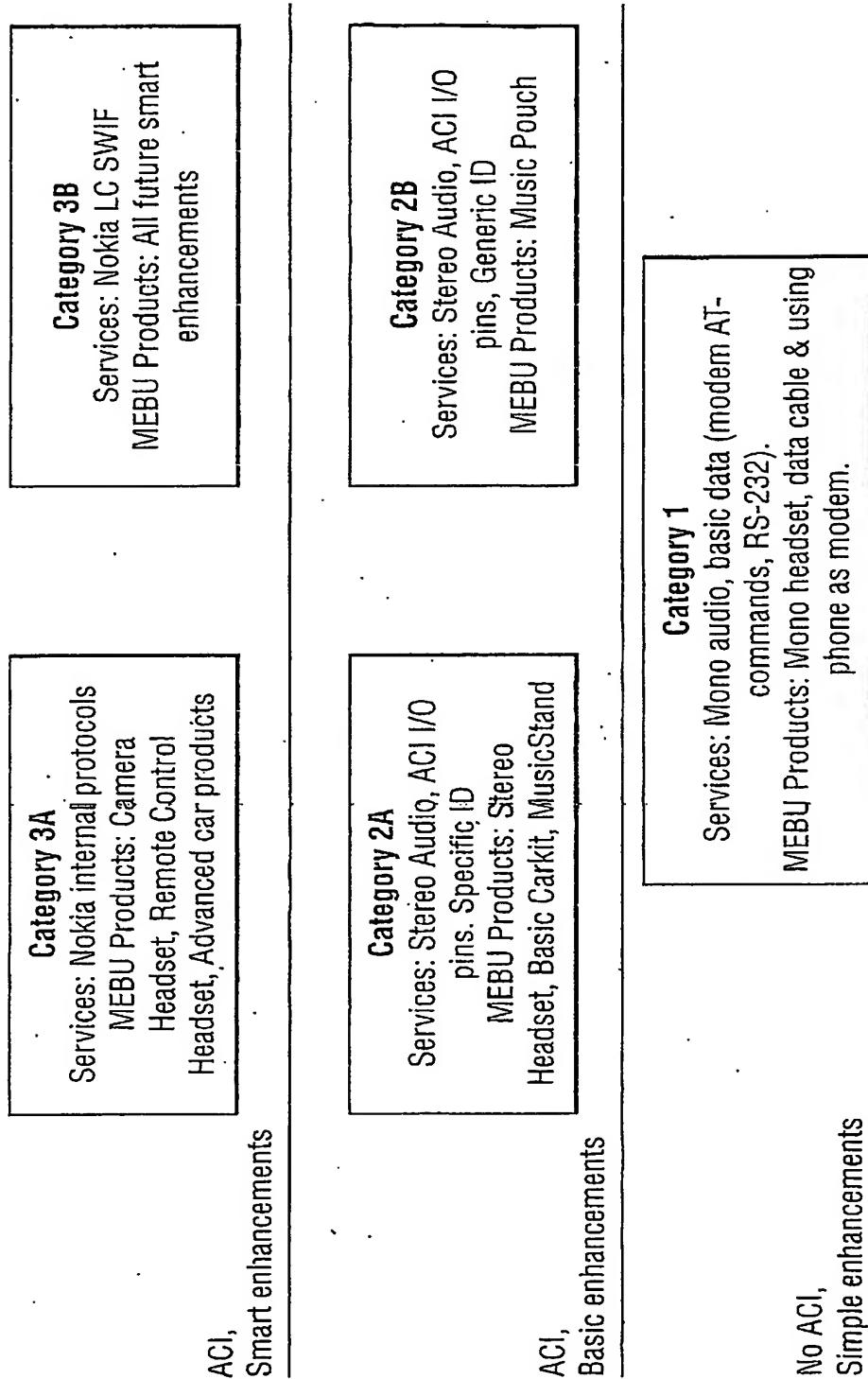


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FIG. 1

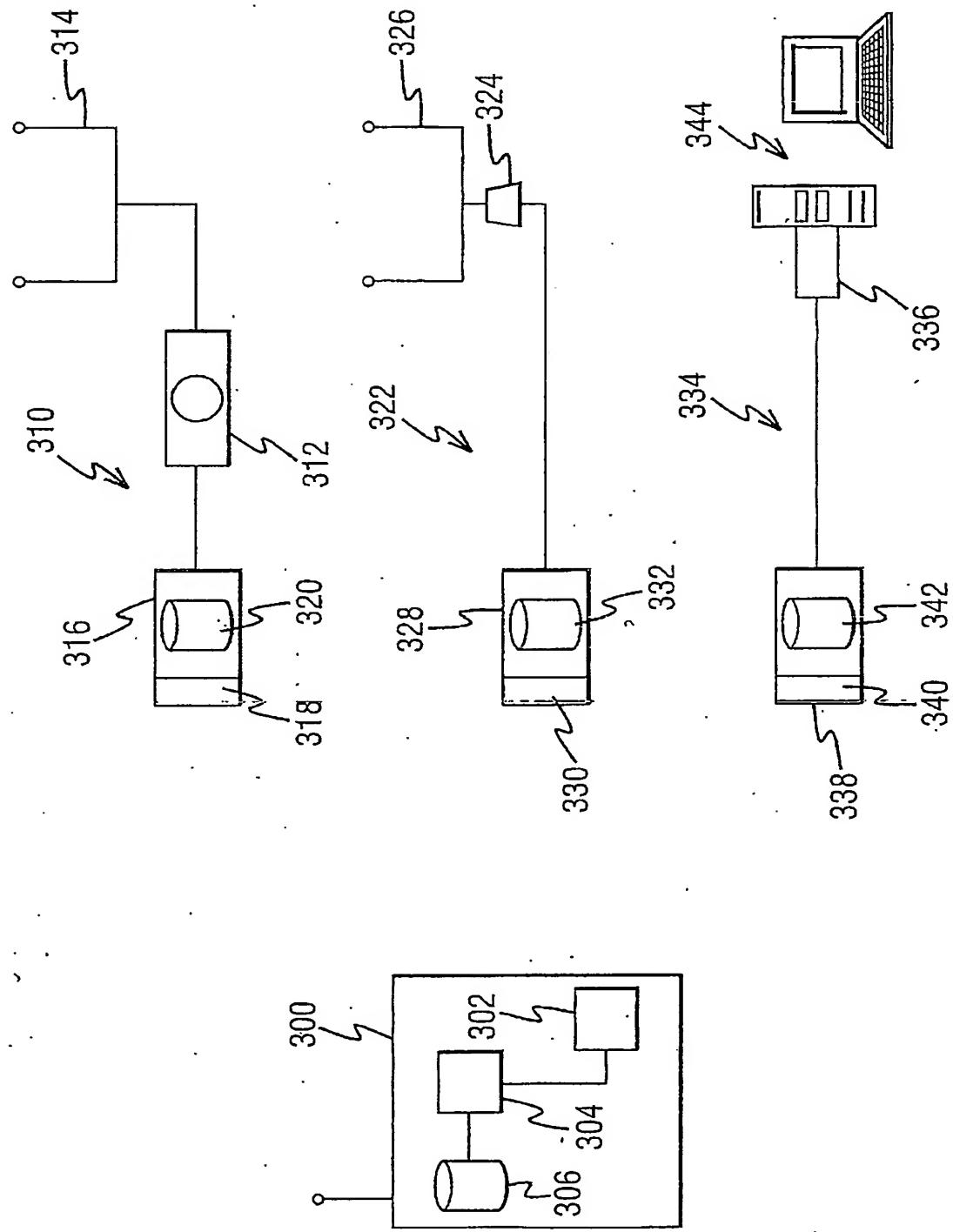
System Block		Value	Size	Bit							Notes
Address	Field			7	6	5	4	3	2	1	
+0	BlockID	1 byte									Block ID
+1	DataLength	1 byte									Byte count from +2, parity byte not included
+2	<u>Acc_type high</u>	1 byte									
	MasterType	6 bit							#	#	MasterType
	SubType MSB	2 bit	#	#	#	#	#	#			SubType high
+3	<u>Acc_type low</u>	1 byte									
	SubType LSB	3 bit				#	#	#	#		SubType low
	Acc_ID	5 bit	#	#	#						Accessory identification number
+4	Parity byte	1 byte									Odd parity for each bit

FIG. 2



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FIG. 3



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FIG: 4

Generic ID System Block		Value	Size	Bit								Notes
Address	Field			7	6	5	4	3	2	1	0	
+0	Block_ID		4 bits					#	#	#	#	Block ID
	Device_ID		12 bits	#	#	#						
												Device_ID
+2	Resource_ID		1 byte									
+3	Device_type		3 bits				#	#	#	#	#	Device type
	Spare		5 bits	#	#	#						Spare
+4	I/O_Inf		8 byte									
			1 bit		#	#	#	#	#	#	#	Feature active state (0/1)
			7 bit									Feature for I/O-pin (0...7)
+12	I/O input/output		1 byte									"1" = Output, '0' = Input

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FIG. 5

Resource_ID field	
Bit	Sub field
0	NBUS
1	FBUS
2	USB
3	Audio
4	Music playback
5	FM antenna
6	Spare
7	Spare

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FIG. 6

Audio Block		Field	Value	Size	Bit	Bit	Bit	Bit	Bit	Bit	Notes
Address					7	6	5	4	3	2	0
+14	Uplink sensitivity			5 bits				#	#	#	Uplink sensitivity (Mic Gain)
				3 bits	#	#	#	#			AEC table selection
+15	Downlink volume										
				5 bits				#	#	#	Downlink sensitivity (Output Gain)
				3 bits	#	#	#	#			Max output level
+16	Enhancement Switchers										
				1 bit	#	#	#	#	#	#	Up alive on/off
				1 bit	#	#	#	#	#	#	Down alive on/off
				2 bits	#	#	#	#	#	#	Output switcher
				1 bit	#	#	#	#	#	#	Mic on/off
				2 bits	#	#	#	#	#	#	DRC target value
				1 bit	#	#	#	#	#	#	Input impedance
+17	Parametric wideband EQ			9 bytes							
+31	Parity byte			1 byte							Odd parity for each bit over the whole block

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FIG. 7

